

FIG. 1

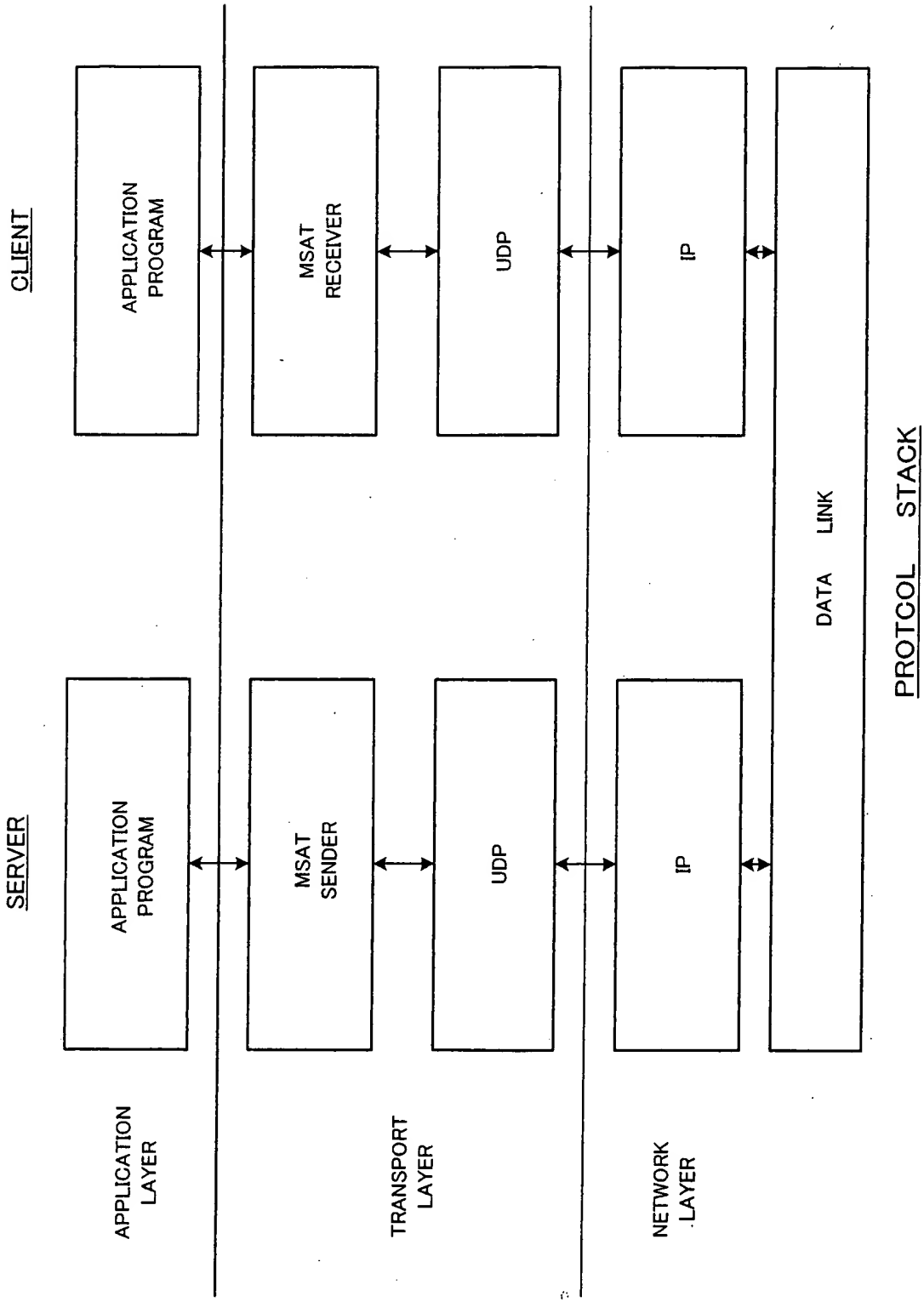


FIG.2

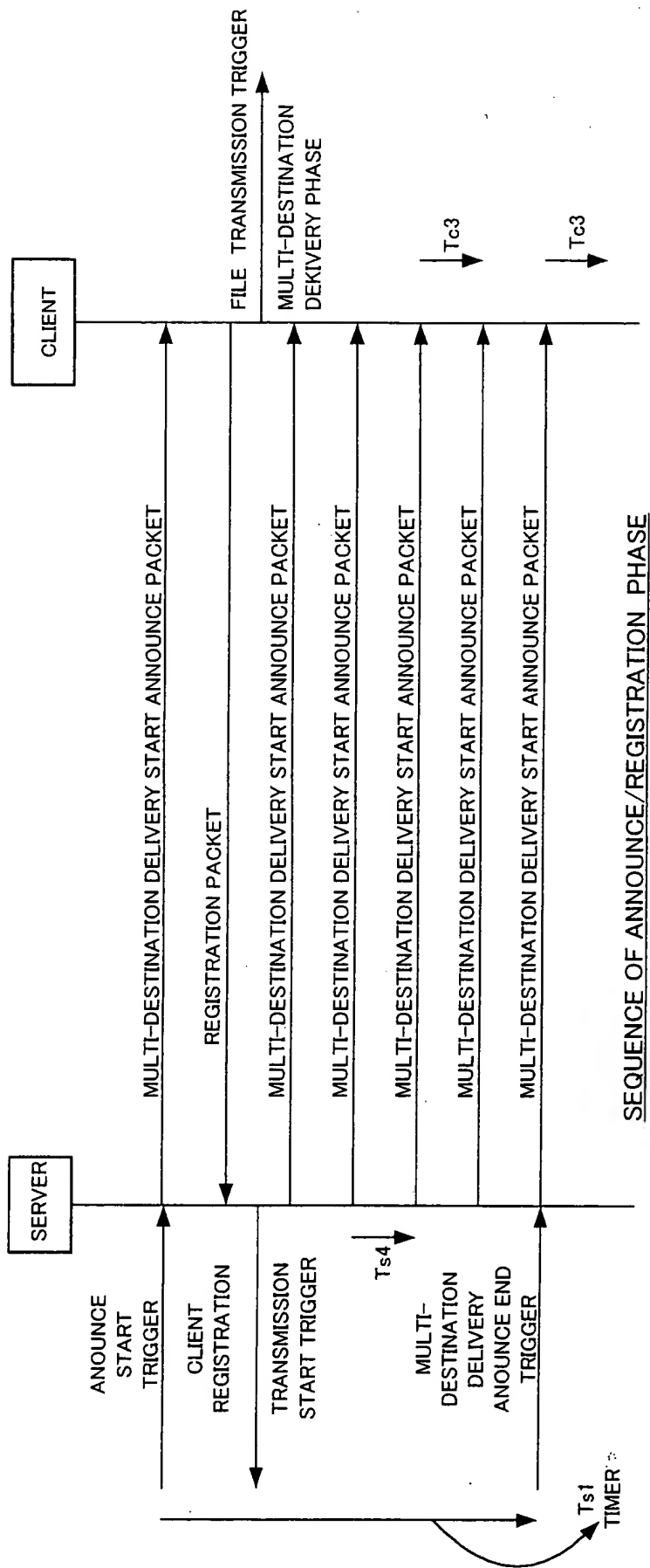
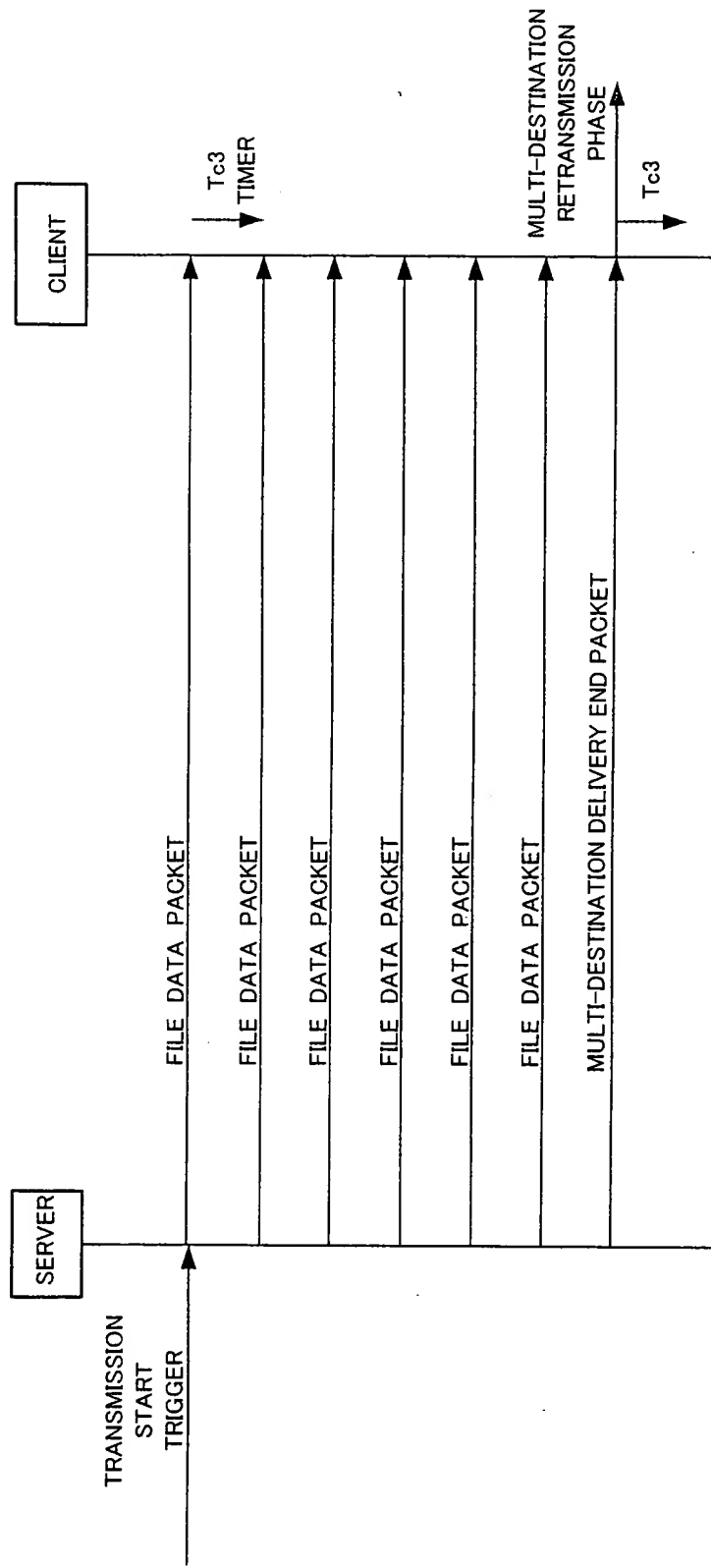
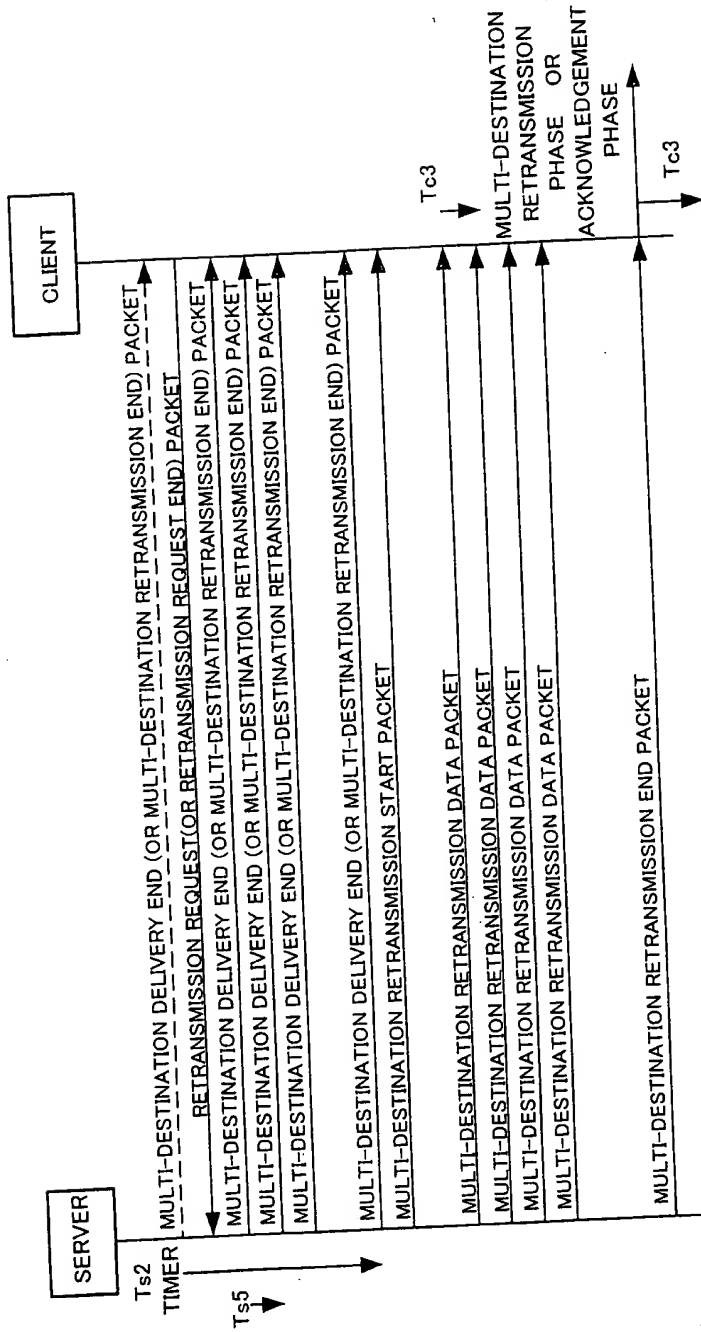


FIG.3



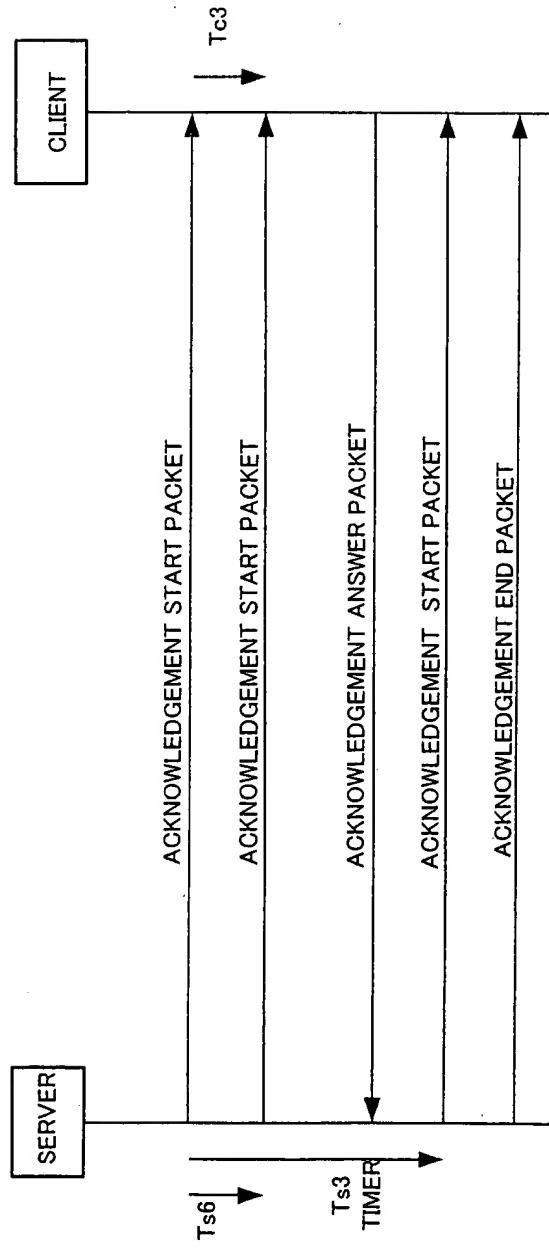
SEQUENCE OF MULTI-DESTINATION DELIVERY PHASE

FIG. 4



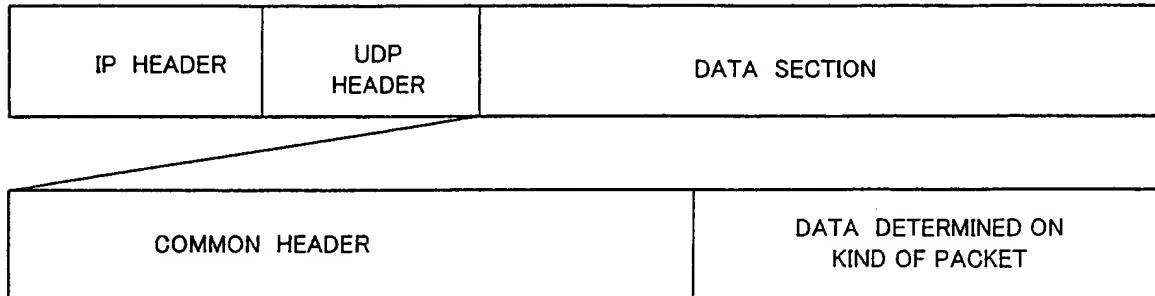
SEQUENCE OF MULTI-DESTINATION RETRANSMISSION PHASE

FIG. 5



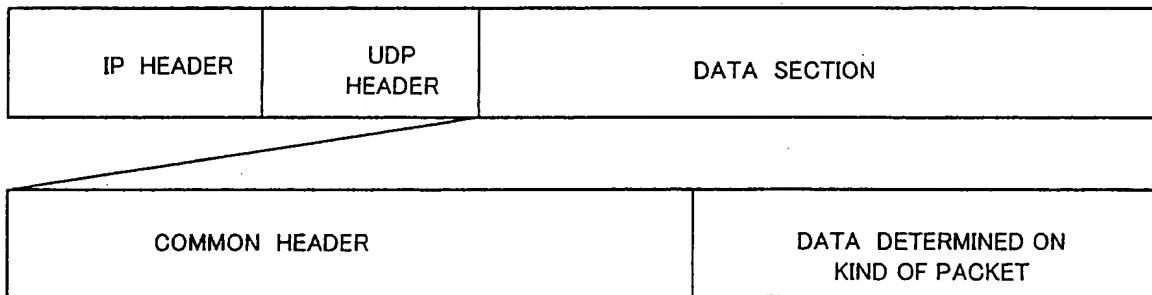
SEQUENCE OF ACKNOWLEDGEMENT PHASE

FIG.6



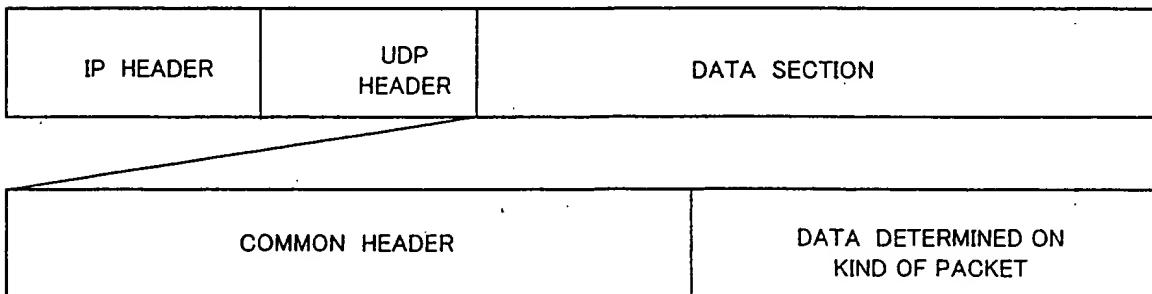
BASIC PACKET FORMAT OF ANNOUNCE REGISTRATION PHASE

FIG.7



BASIC PACKET FORMAT OF MULTI-DESTINATION
DELIVERY PHASE

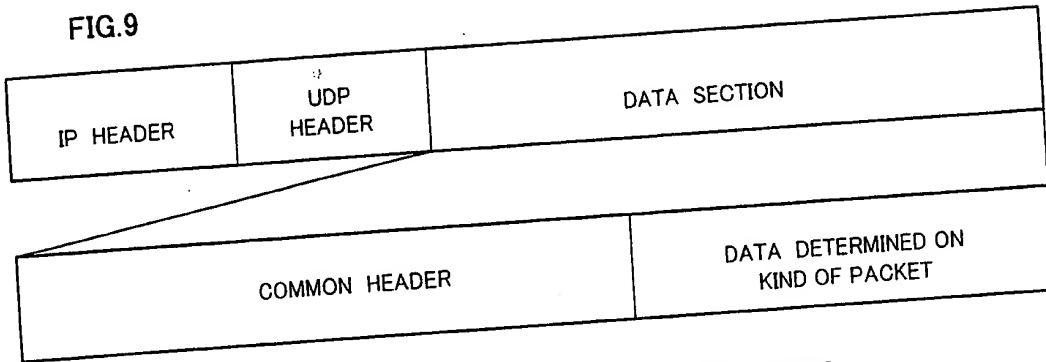
FIG.8



BASIC PACKET FORMAT OF MULTI-DESTINATION
RETRANSMISSION PHASE

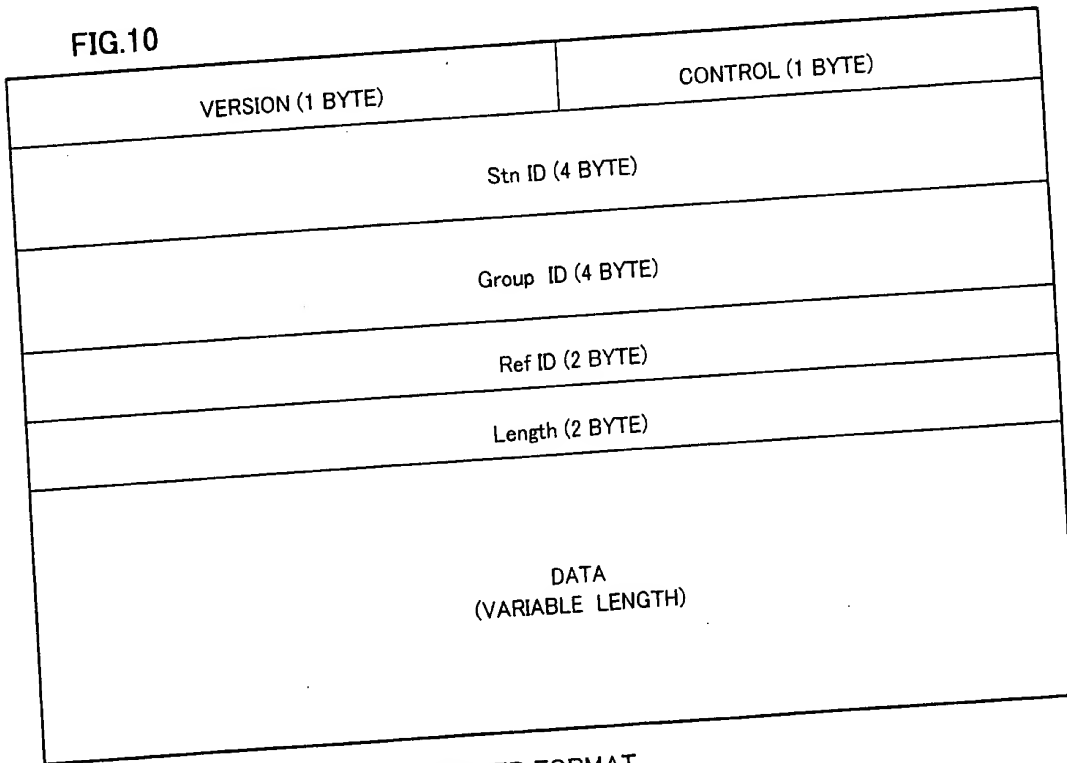
09926137-081001

FIG.9



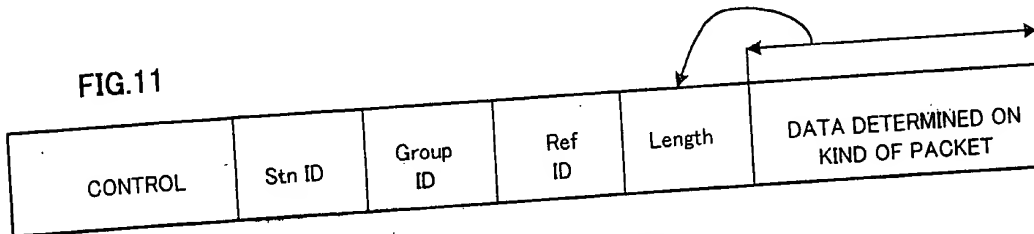
BASIC PACKET FORMAT OF ACKNOWLEDGEMENT PHASE

FIG.10



HEADER FORMAT

FIG.11



DEFINITION OF CODE LENGTH

09928137.081001

090631Z JUL 78 0000Z

090631Z JUL 78 0000Z

090631Z JUL 78 0000Z

090631Z JUL 78 0000Z

[illegible]

090631Z JUL 78 0000Z

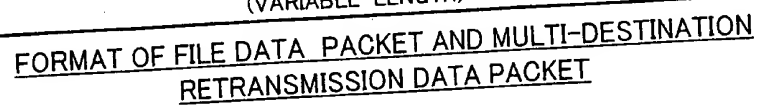
[illegible]

FIG.15

HEADER (14 BYTE)

END PACKET AND

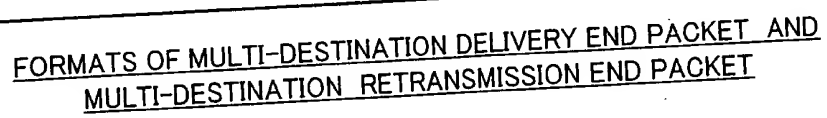


FIG.16

HEADER (14 BYTE)	
MUXIMUM NUMBER OF RETRANSMISSION TIMES (1 BYTE)	RETRANSMISSION TIMES (1 BYTE)
RETRANSMISSION DATA LENGTH (4 BYTE)	
TRANSMISSION RATE (2 BYTE)	
FILE NAME (VARIABLE LENGTH)	

START PACKET

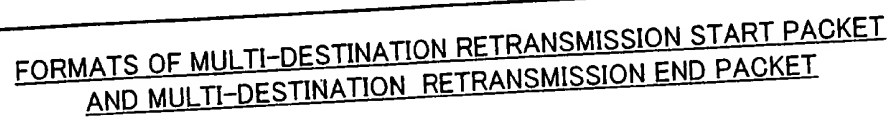
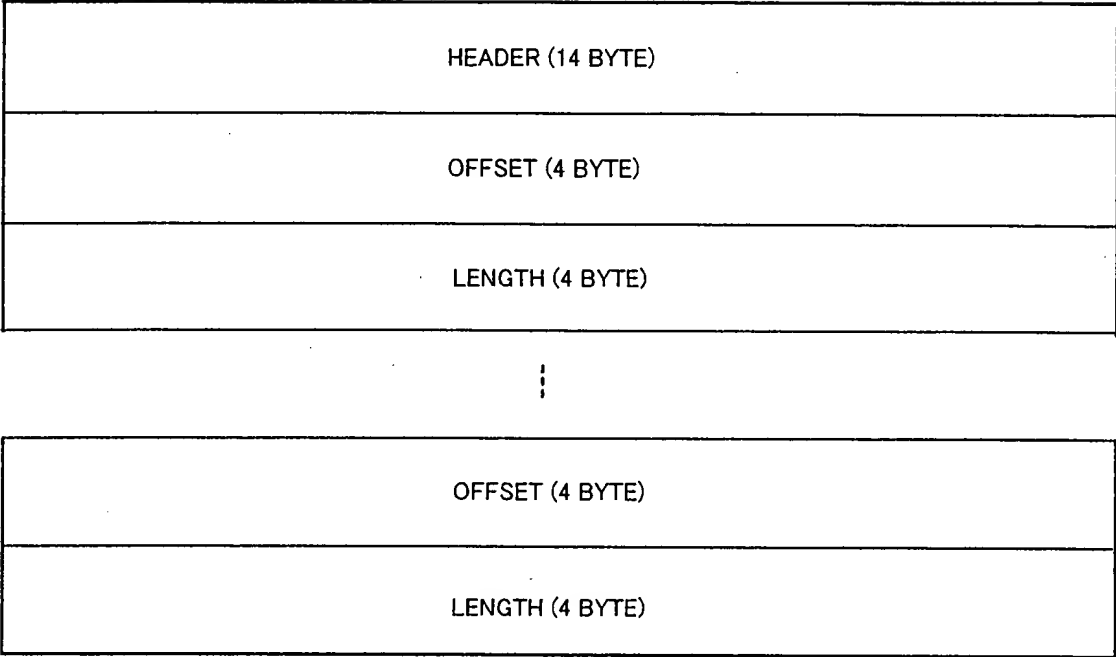
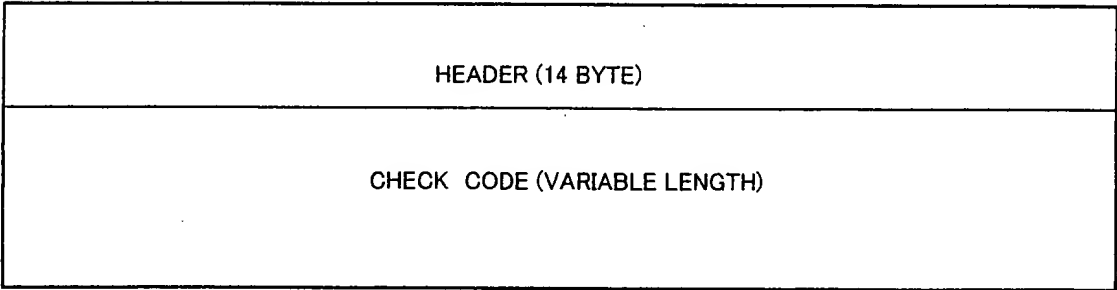


FIG.17

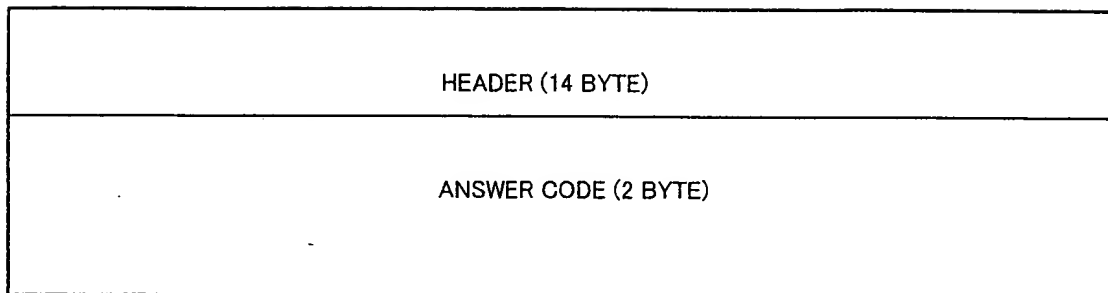


FORMATS OF
RETRANSMISSION REQUEST PACKET AND
RETRANSMISSION REQUEST END PACKET

FIG.18

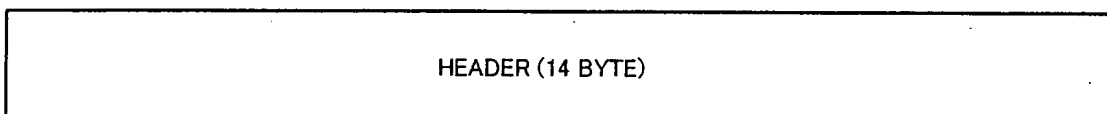


FORMAT OF ACKNOWLEDGEMENT START PACKET

[illegible]

FORMAT OF ACKNOWLEDGEMENT
ANSWER PACKET

FIG.20



FORMAT OF ACKNOWLEDGEMENT
END PACKET

FIG.21A

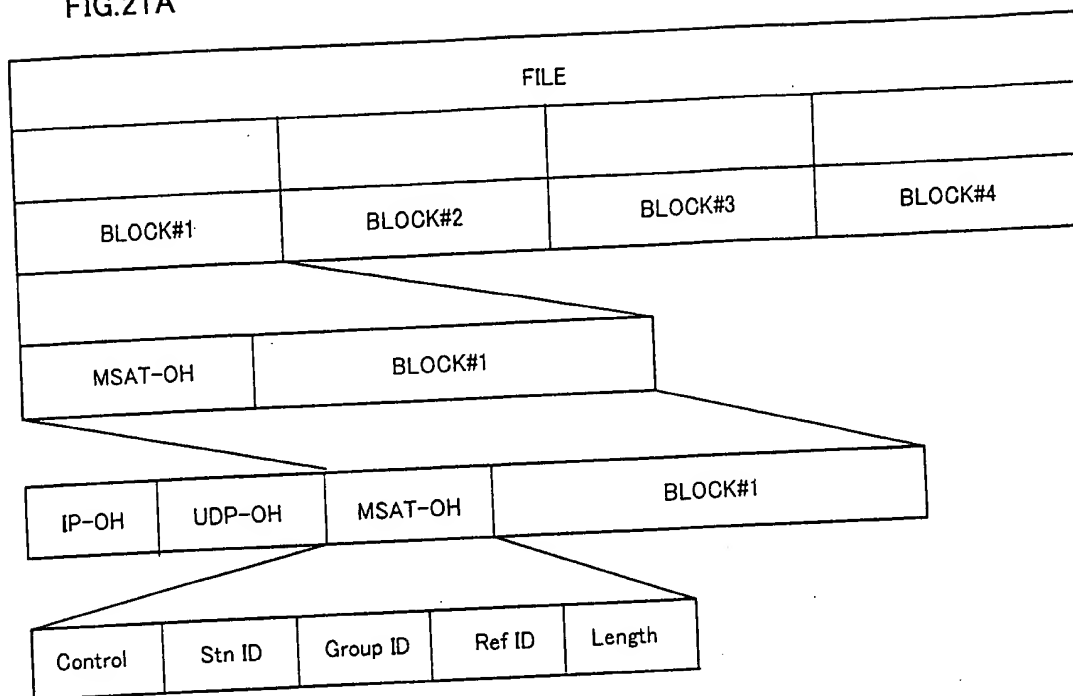


FIG.21B

SEQUENCE NO.	DATA LENGTH	SEQUENCE NO.	DATA LENGTH
--------------	-------------	--------------	-------------

109928137.081001

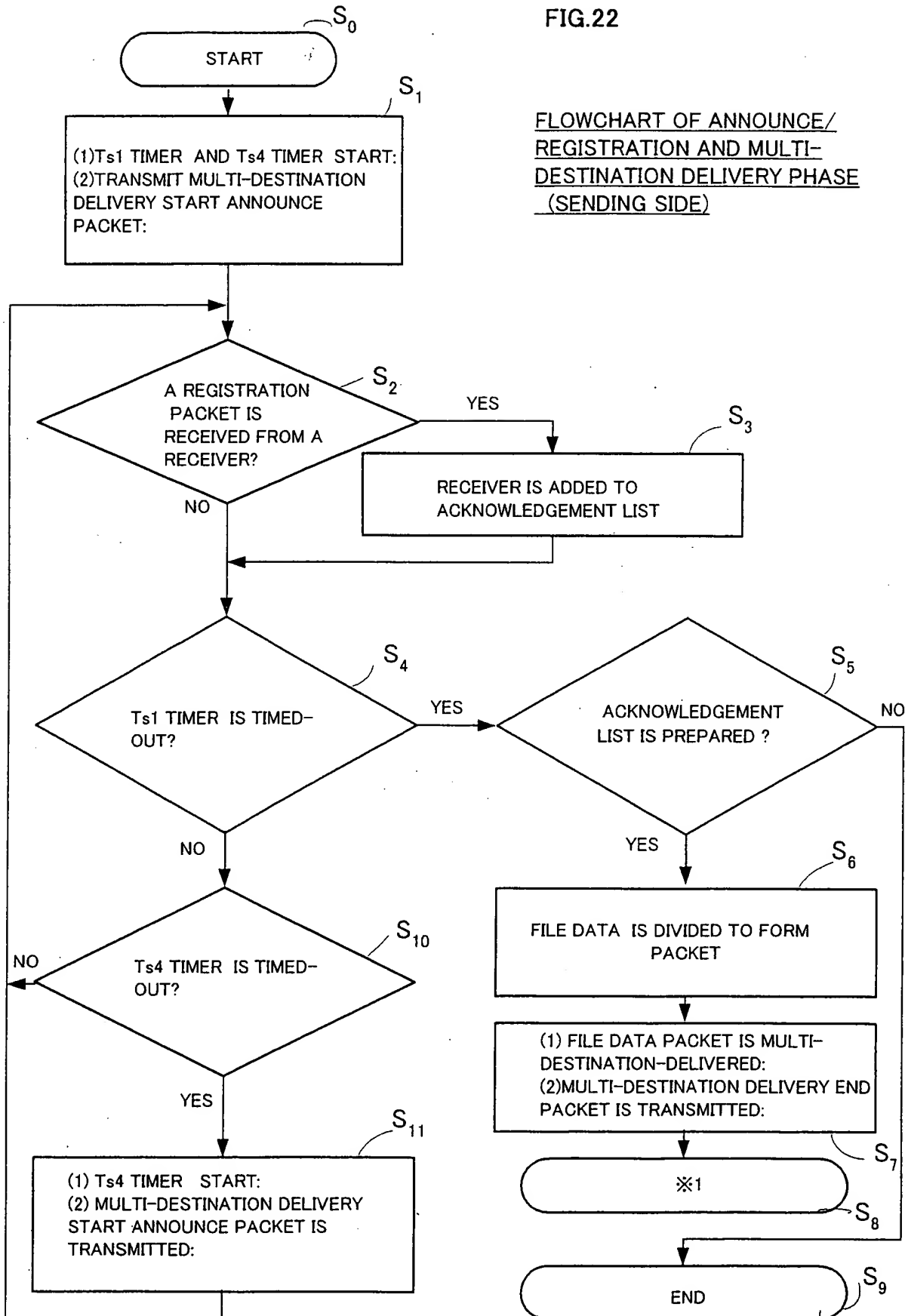


FIG.23

FLOWCHART OF ANNOUNCE/
REGISTRATION AND MULTI-
DESTINATION DELIVERY PHASE
(RECEIVING SIDE)

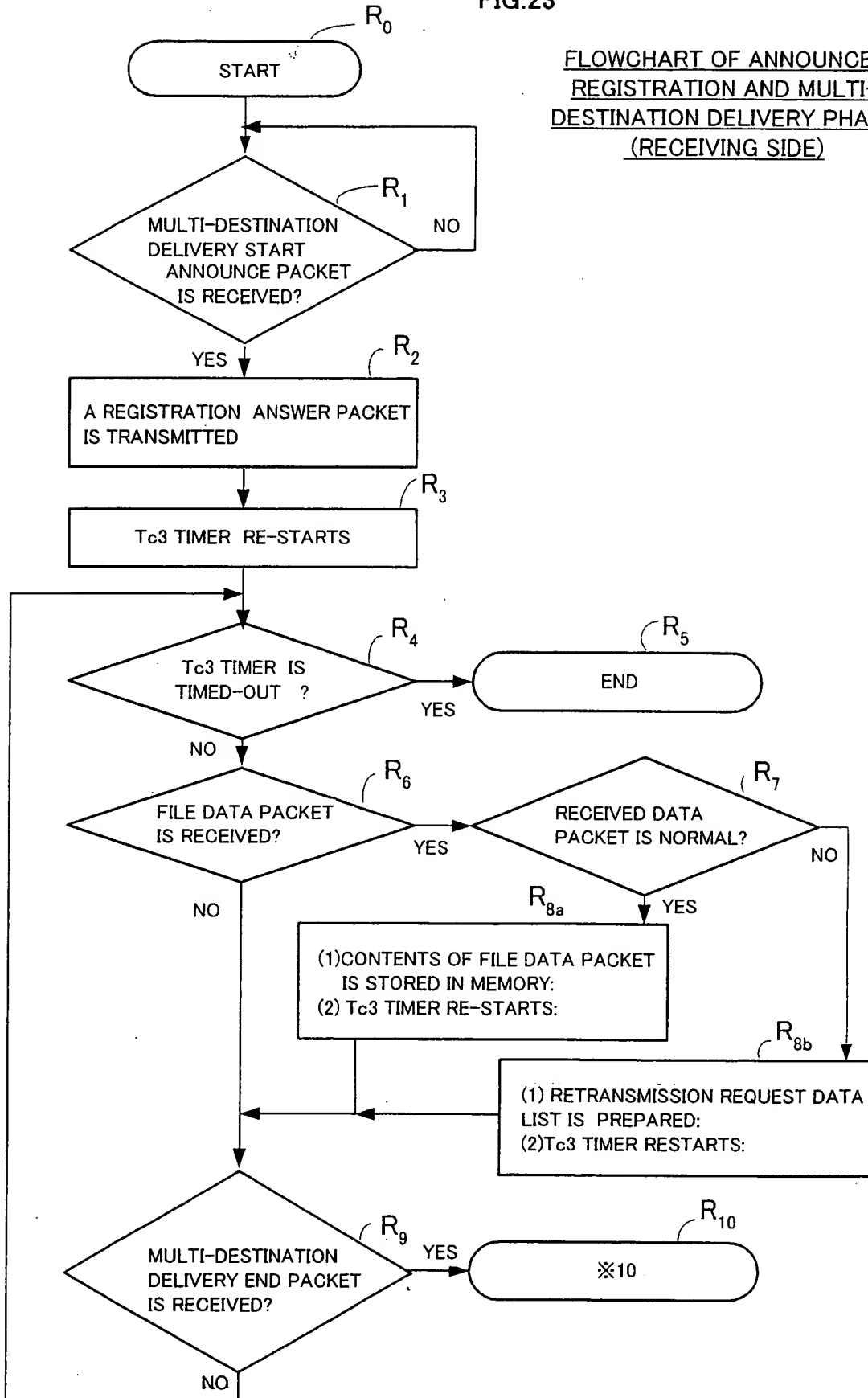


FIG.24

RETRANSMISSION PHASE
(SENDING SIDE)

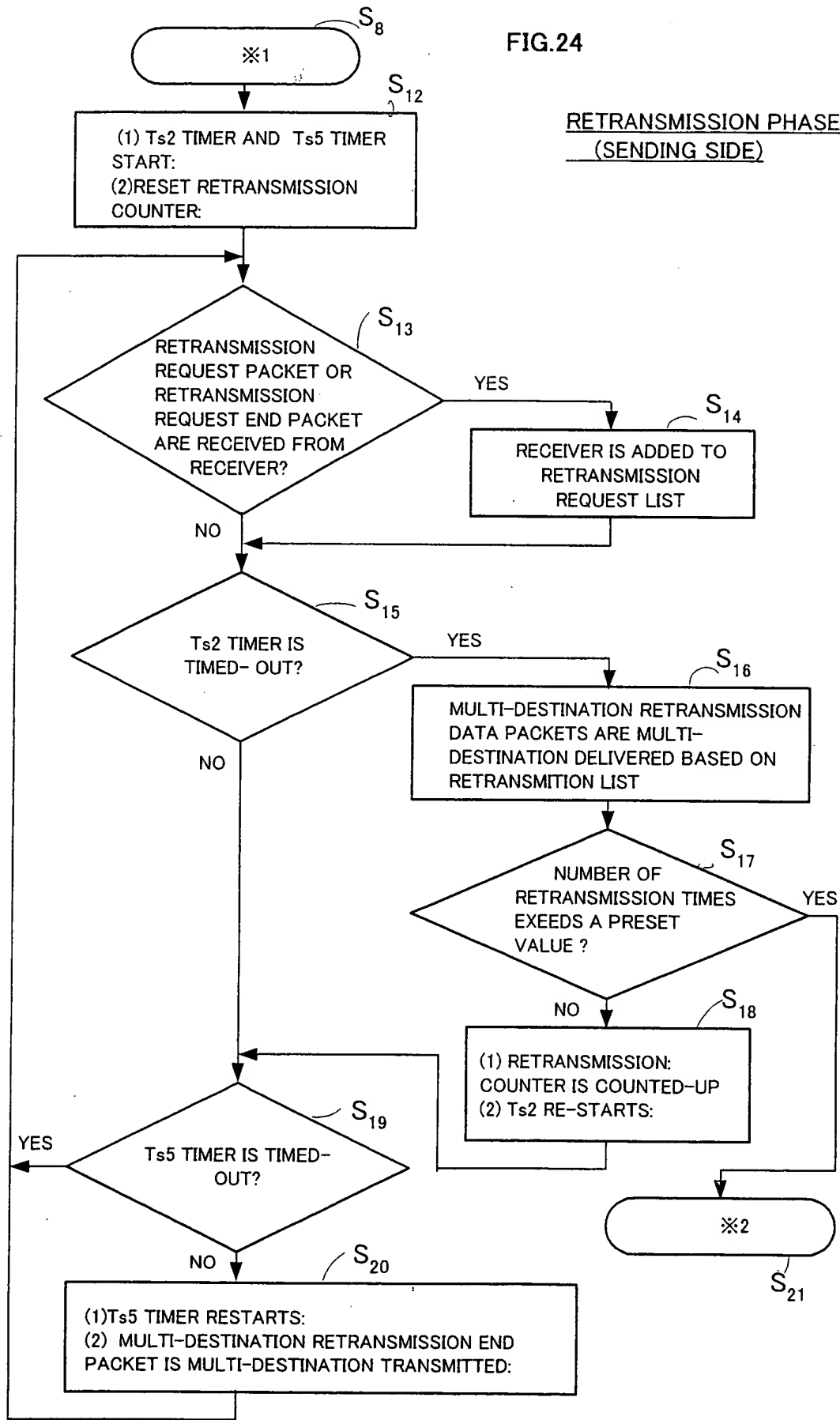
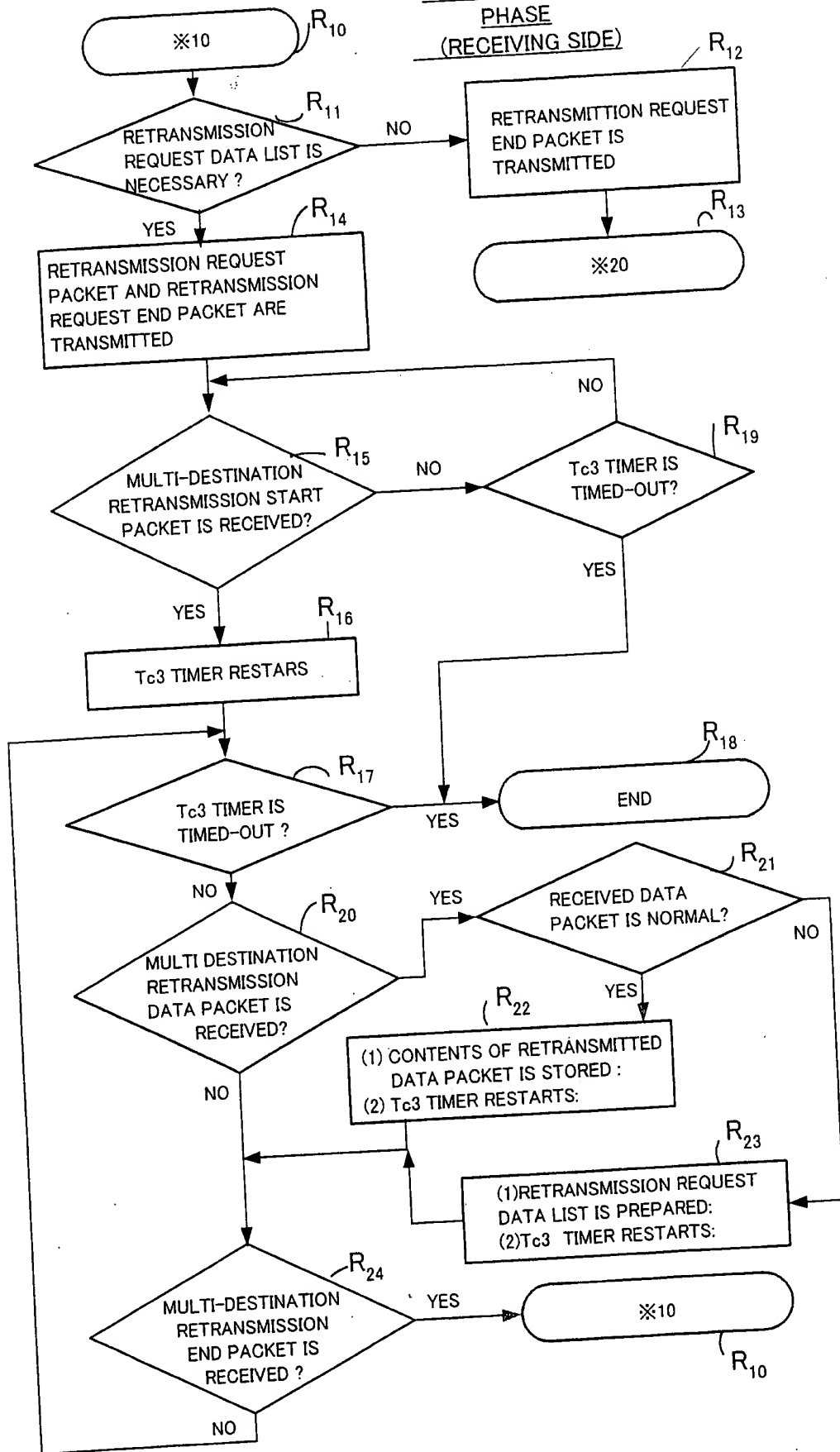


FIG. 24

FIG.25 RETRANSMISSION
PHASE
(RECEIVING SIDE)



TOP SECRET 000000

ACKNOWLEDGEMENT PHASE
(SENDING SIDE)

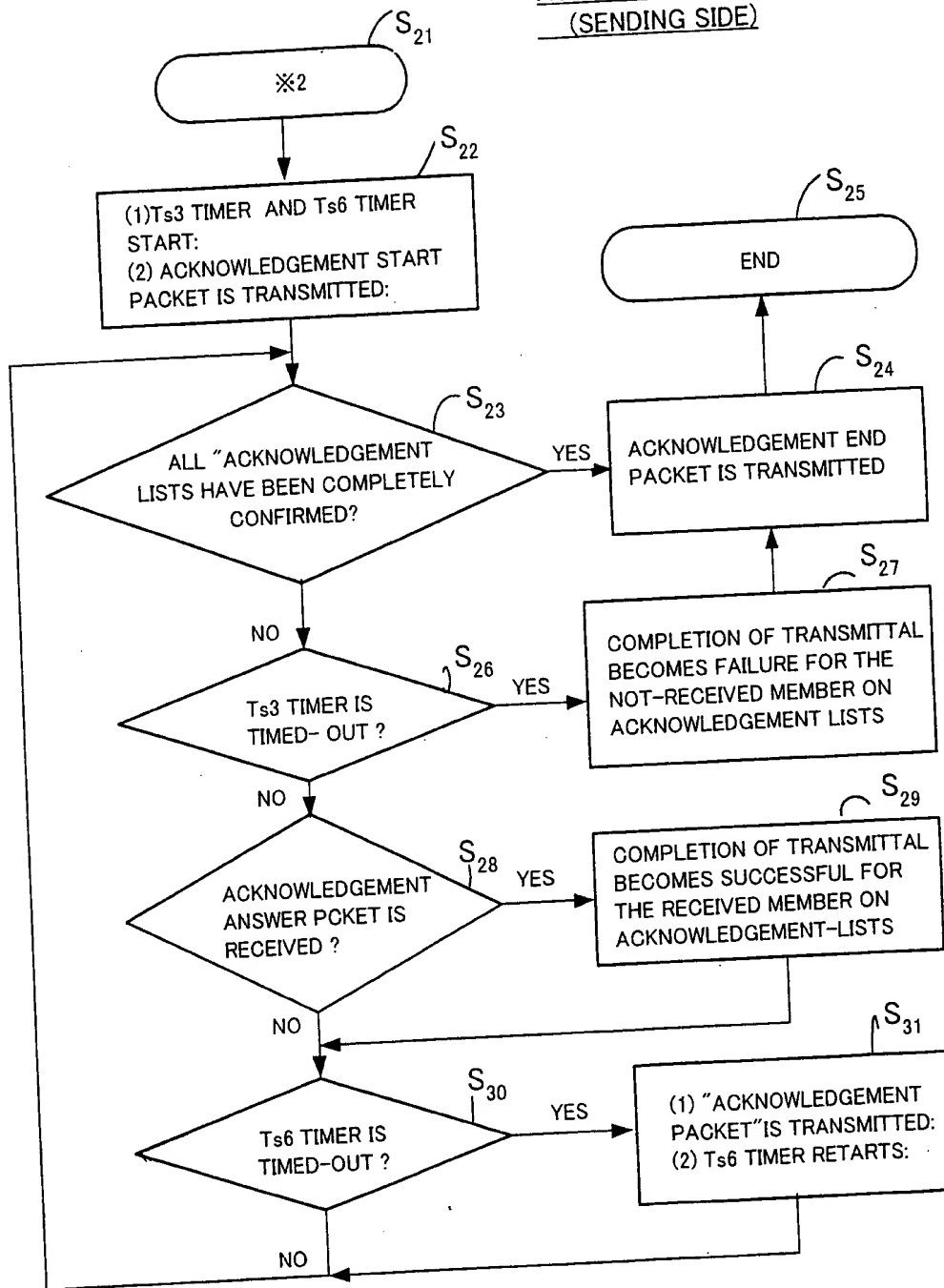
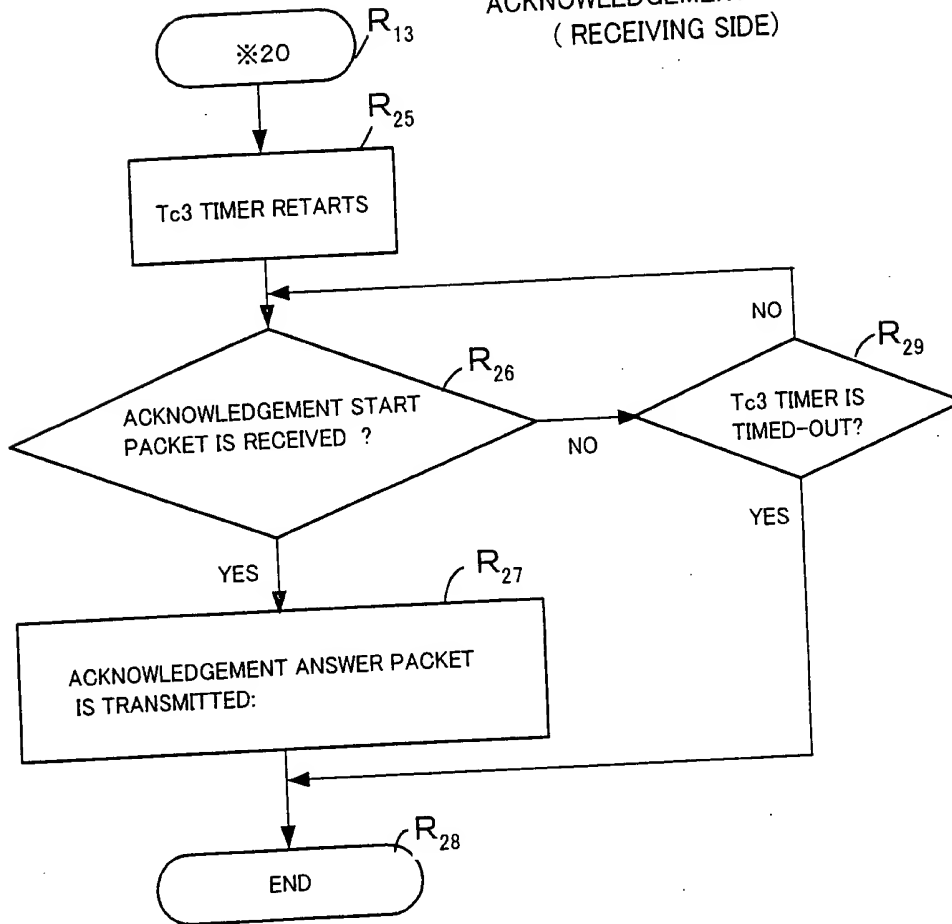


FIG.27

ACKNOWLEDGEMENT PHASE
(RECEIVING SIDE)



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FIG.28A

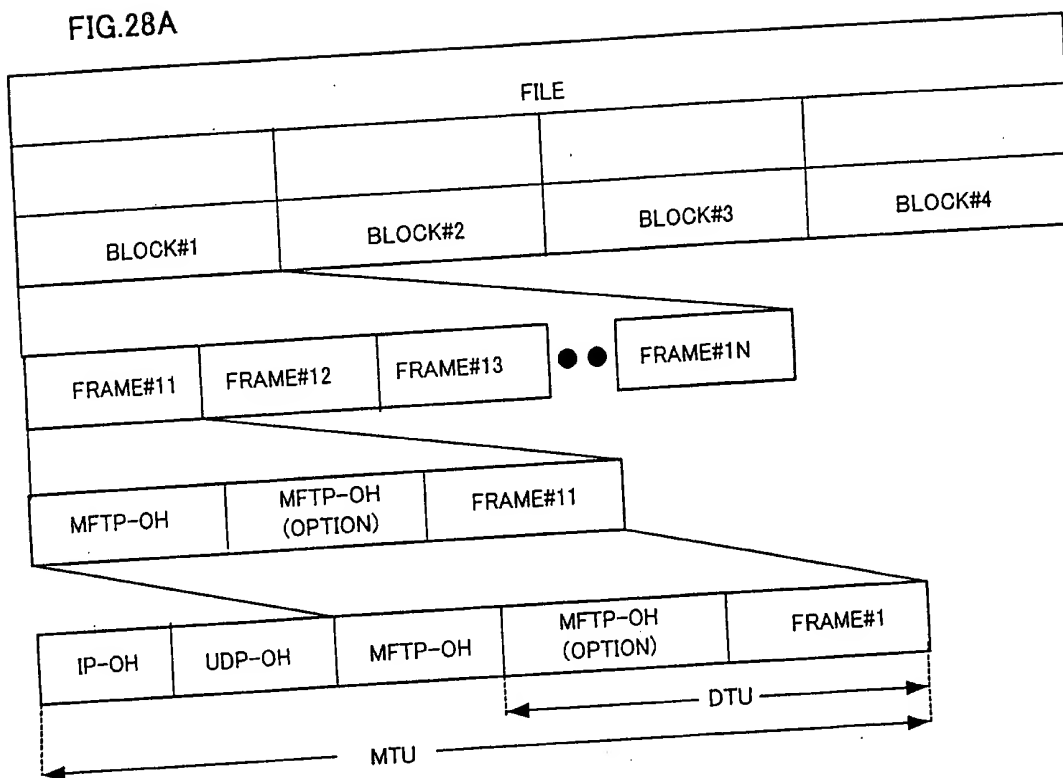


FIG.28B

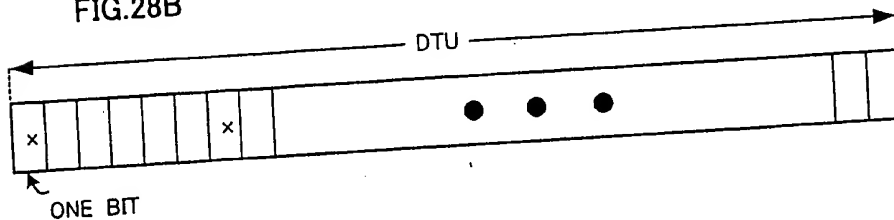


FIG.29

CIRCUIT BIT ERROR RATE

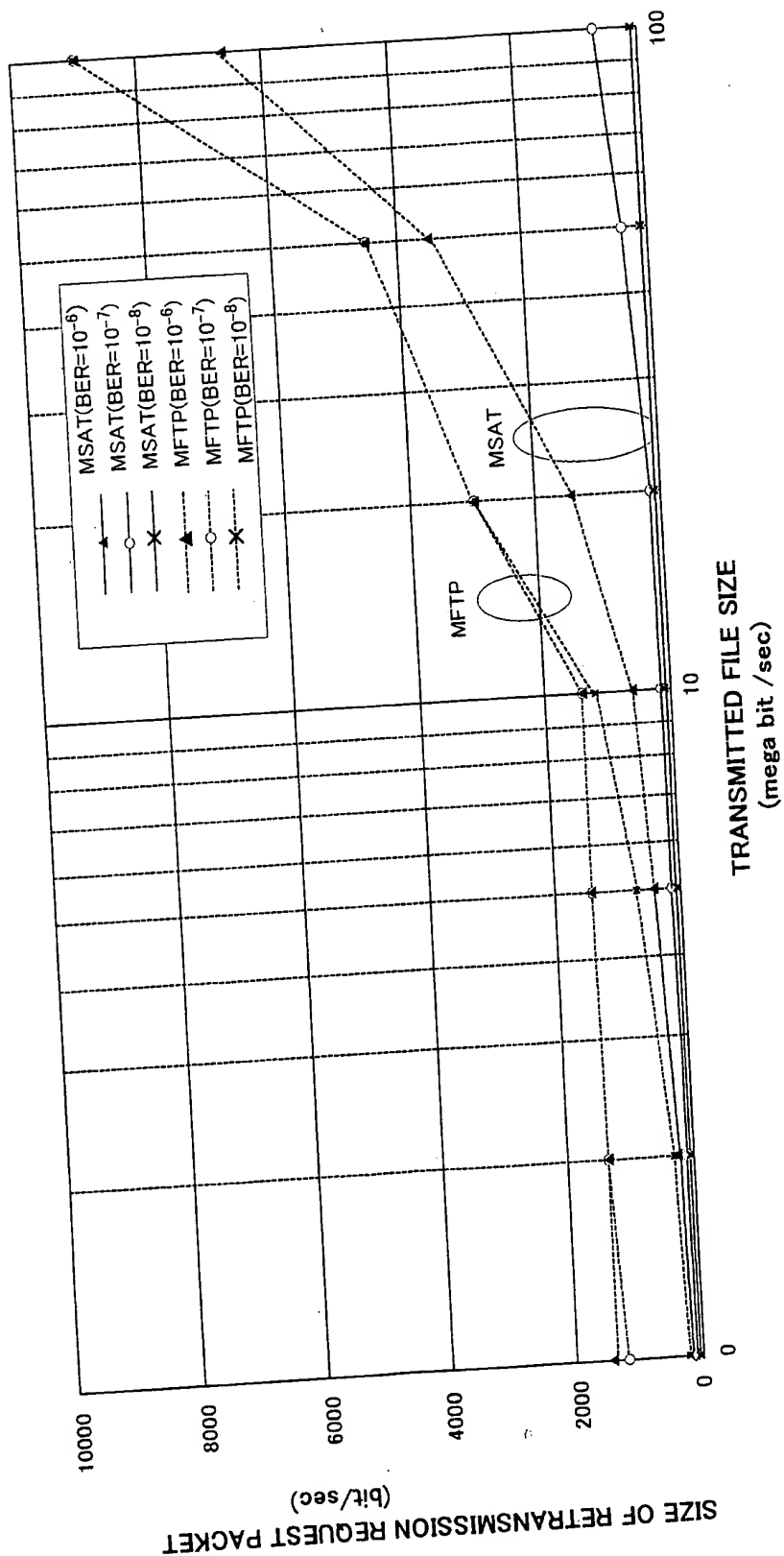


FIG.30

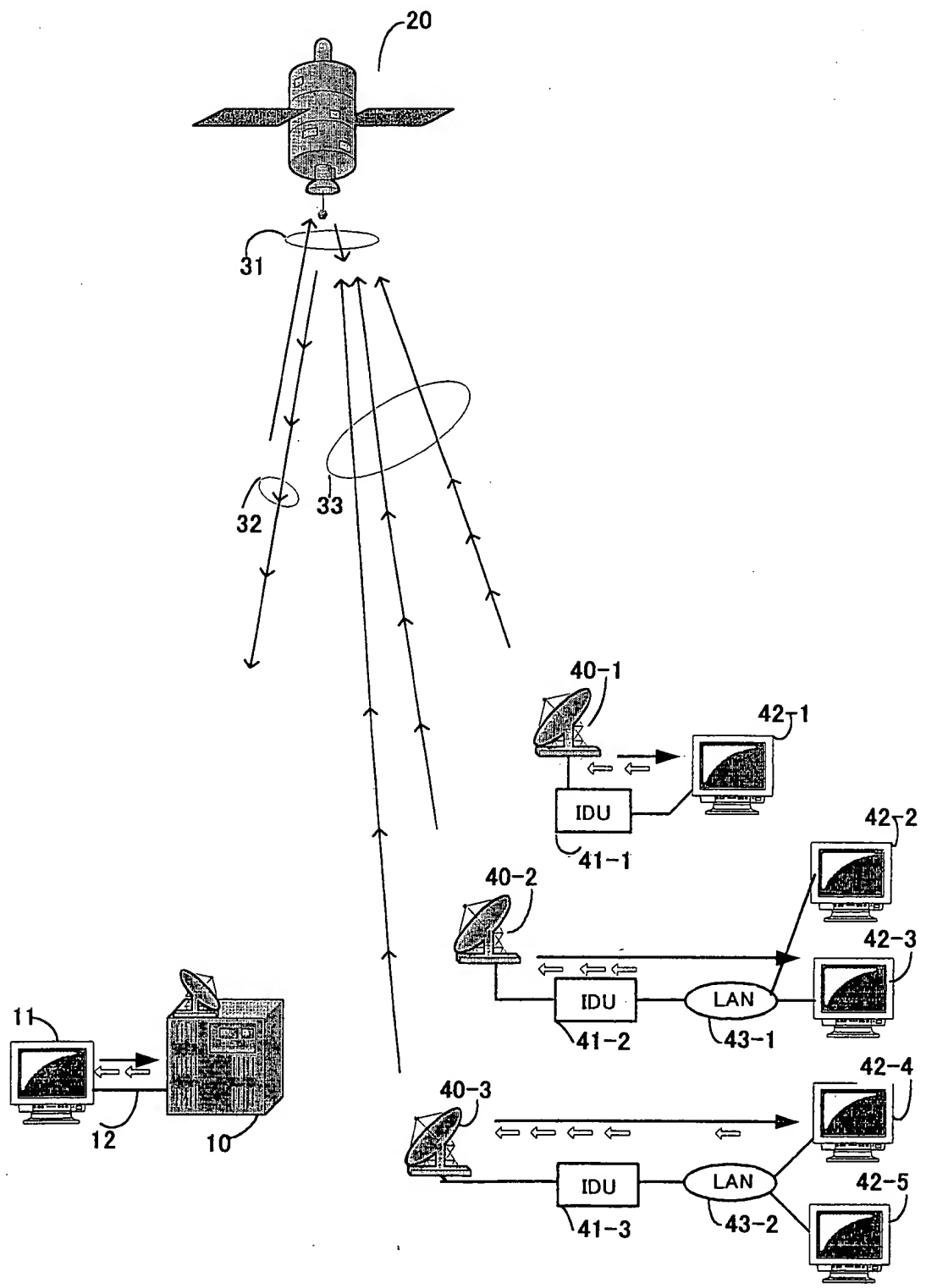


FIG. 30